

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	9	ROM and (on-chip adj error adj correction)	USPAT	OR	ON	2006/05/19 06:35
L3	0	ROM and ((on-chip adj error adj correction) same (row near address) same (column near address))	USPAT	OR	ON	2006/05/19 06:35
L4	0	((on-chip adj error adj correction) same (row near address) same (column near address))	USPAT	OR	ON	2006/05/19 06:36
L5	0	((on-chip with error adj correction) same (row near address) same (column near address))	USPAT	OR	ON	2006/05/19 06:36
L6	0	((on-chip with ((error adj correction) ECC)) same (row near address) same (column near address))	USPAT	OR	ON	2006/05/19 06:36
L7	0	((on-chip with ((error adj (correction correcting)) ECC)) same (row near address) same (column near address))	USPAT	OR	ON	2006/05/19 06:37
L8	0	((on adj chip) with ((error adj (correction correcting)) ECC)) same (row near address) same (column near address))	USPAT	OR	ON	2006/05/19 06:37
L9	1	((on adj chip) embedded) with ((error adj (correction correcting)) ECC) same (row near address) same (column near address))	USPAT	OR	ON	2006/05/19 06:39
L10	4	((error adj (correction correcting)) ECC) same (row near address) same (column near address)) and ((on adj chip) embedded) with ((error adj (correction correcting)) ECC)	USPAT	OR	ON	2006/05/19 06:41
L11	11	((error adj (correction correcting)) ECC) same (row near address) same (column near address)) and ((on adj chip) on-chip embedded) with ((error adj (correction correcting)) ECC)	USPAT	OR	ON	2006/05/19 06:41
L12	11	((error adj (correction correcting)) ECC) same (row near address) same (column near address)) and ((on adj chip) on-chip embedded) with ((error adj (correction correcting)) ECC)	USPAT	OR	ON	2006/05/19 07:15
L14	10	((error adj (correction correcting)) ECC) same (row near address) same (column near address)) and ((on adj chip) on-chip embedded) with ((error adj (correction correcting)) ECC) and (DRAM ROM)	USPAT	OR	ON	2006/05/19 07:26

## EAST Search History

L16	1	DRAN with rom	USPAT	OR	ON	2006/05/19 07:17
L17	4852	DRAm with rom	USPAT	OR	ON	2006/05/19 07:18
L18	102	DRAm with rom with embedded	USPAT	OR	ON	2006/05/19 07:18
L19	57	(DRAm with rom).ab.	USPAT	OR	ON	2006/05/19 07:21
L20	2332	(DRAm with rom).ab. dram.ti.	USPAT	OR	ON	2006/05/19 07:18
L21	19	(DRAm with rom).ab. and dram.ti.	USPAT	OR	ON	2006/05/19 07:19
L22	38	19 not 21	USPAT	OR	ON	2006/05/19 07:22
L23	3	((error adj (correction correcting)) ECC) same (row near address) same (column near address)) and ((on adj chip) on-chip embedded) with ((error adj (correction correcting)) ECC)) and (DRAM ROM) and rom	USPAT	OR	ON	2006/05/19 07:27
L24	8	((error adj (correction correcting)) ECC) same (row near address) same (column near address)) and ((on adj chip) on-chip embedded) with ((error adj (correction correcting)) ECC)) and (DRAM ROM) and dram	USPAT	OR	ON	2006/05/19 07:28